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APPLICANTS

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** CONTINUING DATA *****

This application is a 371 of PCT/IB03/03429 08/04/2003

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 02078785.9 09/13/2002

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY NETHERLANDS	SHEETS DRAWING 3	TOTAL CLAIMS 10 7	INDEPENDENT CLAIMS 8 2
35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met				
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>		

ADDRESS

24738

TITLE

Reduced chip testing scheme at wafer level

FILING FEE RECEIVED 1260	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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